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jc997 U.S. PTO
09/998131
12/03/01

Applicant(s): PARK, Ji Soo; SOHN, Dong

Application No.:

Group:

Filed: August 20, 1999

Examiner:

For: SEMICONDUCTOR DEVICE HAVING A GATE AND FABRICATION METHOD
THEREFOR

LETTER

Assistant Commissioner for Patents
Box Patent Application
Washington, D.C. 20231

August 20, 1999
0465-0561P-SP

Sir:

Under the provisions of 35 USC 119 and 37 CFR 1.55(a), the applicant hereby claims the right of priority based on the following application(s):

<u>Country</u>	<u>Application No.</u>	<u>Filed</u>
REPUBLIC OF KOREA	42340/1998	10/09/98

A certified copy of the above-noted application(s) is(are) attached hereto.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By: 

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09/998131



12/03/01

별첨 사본은 아래 출원의 원본과 동일함을 증명함.

This is to certify that the following application annexed hereto
is a true copy from the records of the Korean Industrial
Property Office.

출원번호 : 1998년 특허출원 제42340호
Application Number

출원년월일 : 1998년 10월 9일
Date of Application

출원인 : 엘지반도체주식회사
Applicant(s)



199 8 년 11 월 9 일

특 허 청

COMMISSIONER

